

IN THE ABSTRACT:

Please substitute the abstract on the accompanying separate sheet for the abstract appearing on page 79.

IN THE CLAIMS:

1 1. (Amended) An integrated circuit (IC) card
2 comprising:
3 a card substrate including,
4 a semiconductor integrated circuit chip mounted
5 thereon; and
6 a plurality of connector terminals formed
7 thereon;
8 said connector terminals being exposed from a casing;
9 wherein said connector terminals are laid out in first
10 and second sequences staggered relatively in a sequence
11 direction and adjacent to one another forward and backward
12 as viewed in an IC card inserting direction, and
13 wherein the first sequence includes a source voltage
14 supply terminal, and the second sequence is devoid of
15 connector terminals at a position adjacent to the source
16 voltage supply terminal and at positions adjacent to
17 terminal-to-terminal areas at opposite sides of the source
18 voltage supply terminal.

1 1.3. (Amended) An integrated circuit (IC) card
2 comprising:
3 a card substrate including,
4 a semiconductor integrated circuit chip mounted
5 thereon; and
6 a plurality of connector terminals formed
7 thereon;
8 said connector terminals being exposed from a casing;
9 wherein said connector terminals include an
10 arrangement of first and second sequences formed back and
11 forth as viewed in an IC card inserting direction, and
12 shifted from one another in a sequence direction, and
13 wherein the first sequence includes a source voltage
14 supply terminal, and the second sequence is devoid of
15 connector terminals at a position adjacent to the source
16 voltage supply terminal and at positions adjacent to
17 terminal-to-terminal areas at opposite sides of the source
18 voltage supply terminal.

1 1.4. (Amended) The IC card according to claim 1,
2 wherein a connector terminal at one end of the second
3 sequence extends to a position adjoining a connector